

Electronic Patent Application Fee Transmittal

Application Number:	10753852			
Filing Date:	08-Jan-2004			
Title of Invention:	Scalable logic self-test configuration for multiple chips			
First Named Inventor:	Marvin J. Rich			
Filer:	Jill M. Breedlove			
Attorney Docket Number:	POU920030173US1			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 2 months with \$0 paid	1252	1	450	450

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				450